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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yeo, *et al.* Docket No.: TSM03-0553  
Serial No.: 10/667,871 Art Unit: 2812  
Filed: September 22, 2003 Examiner: Jennifer Kennedy  
For: Resistor with Reduced Leakage

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Respectfully submitted,

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yeo, *et al.*

Docket No.: TSM03-0553

Filed: 9/22/2003

Examiner: Jennifer M. Kennedy

Serial No.: 10/667,871

Art Unit: 2812

Title: Resistor with Reduced Leakage

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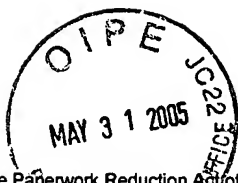
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PTO/SB/08a (08-03)

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STATEMENT BY APPLICANT**

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**Complete if Known**

Application Number	10/667,871
Filing Date	9/22/2003
First Named Inventor	Yeo, et al.
Art Unit	2812
Examiner Name	Jennifer Kennedy
Attorney Docket Number	TSM03-0553

Sheet	1	of	2
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**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
	1	US-4,069,094	01-17-1978	Shaw, et al.	
	2	US-4,497,683	02-05-1985	Celler, et al.	
	3	US-5,155,571	10-13-1992	Wang, et al.	
	4	US-5,378,919	01-03-1995	Ochiai	
	5	US-5,479,033	12-26-1995	Baca, et al.	
	6	US-5,708,288	01-13-1998	Quigley, et al.	
	7	US-5,789,807	08-04-1998	Correale, Jr.	
	8	US-5,955,766	09-21-1999	Ibi, et al.	
	9	US-6,111,267	08-29-2000	Fischer, et al.	
	10	US-6,256,239 B1	07-03-2001	Akita, et al.	
	11	US-6,339,232 B1	01-15-2002	Takagi	
	12	US-6,407,406 B1	06-18-2002	Tezuka	
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	17	US-6,573,172 B1	06-03-2003	En, et al.	
	18	US-6,576,526 B2	06-10-2003	Kai, et al.	
	19	US-6,600,170 B1	07-29-2003	Xiang	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
	20	EP 0683522 A2	11-22-1995	International Business Machines Corporation		
	21	EP 0828296 A2	03-11-1998	International Business Machines Corporation		

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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	10/667,871
				Filing Date	9/22/2003
				First Named Inventor	Yeo, <i>et al.</i>
				Art Unit	2812
				Examiner Name	Jennifer Kennedy
Sheet	2	of	2	Attorney Docket Number	TSM03-0553

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
	22	US-6,617,643 B1	09-09-2003	Goodwin-Johansson	
	23	US-6,633,070 B2	10-14-2003	Miura, <i>et al.</i>	
	24	US-6,657,276 B1	12-02-2003	Karlsson, <i>et al.</i>	
	25	US-6,720,619 B1	04-13-2004	Chen, <i>et al.</i>	
	26	US-6,724,019 B2	04-20-2004	Oda, <i>et al.</i>	
	27	US-6,759,717 B2	07-06-2004	Sagarwala, <i>et al.</i>	
	28	US-6,762,448 B1	07-13-2004	Lin, <i>et al.</i>	
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	31	US-2002/0031890 A1	03-14-2002	Watanabe, <i>et al.</i>	
	32	US-2002/0074598 A1	06-20-2002	Doyle, <i>et al.</i>	
	33	US-2002/0125471 A1	09-12-2002	Fitzgerald, <i>et al.</i>	
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	35	US-2005/0029601 A1	02-10-2005	Chen, <i>et al.</i>	

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
	36	LEITZ, C. W., <i>et al.</i> , "Hole mobility enhancements in strained Si/Si <sub>1-x</sub> Ge <sub>x</sub> p-type metal-oxide-semiconductor field-effect transistors grown on relaxed Si <sub>1-x</sub> Ge <sub>x</sub> (x<y) virtual substrates," Applied Physics Letters, December 17, 2001, pp. 4246-4248, Volume 79, Number 25.		
	37	LEITZ, C. W., <i>et al.</i> , "Channel Engineering of SiGe-Based Heterostructures for High Mobility MOSFETs," Mat. Res. Soc. Symp. Proc., 2002, pp. 113-118, Vol. 686.		
	38	LIU, K. C., <i>et al.</i> , "A Novel Sidewall Strained-Si Channel nMOSFET," IEDM, 1999, pp. 63-66.		

Examiner Signature		Date Considered	
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